TOSHIBA 2705860

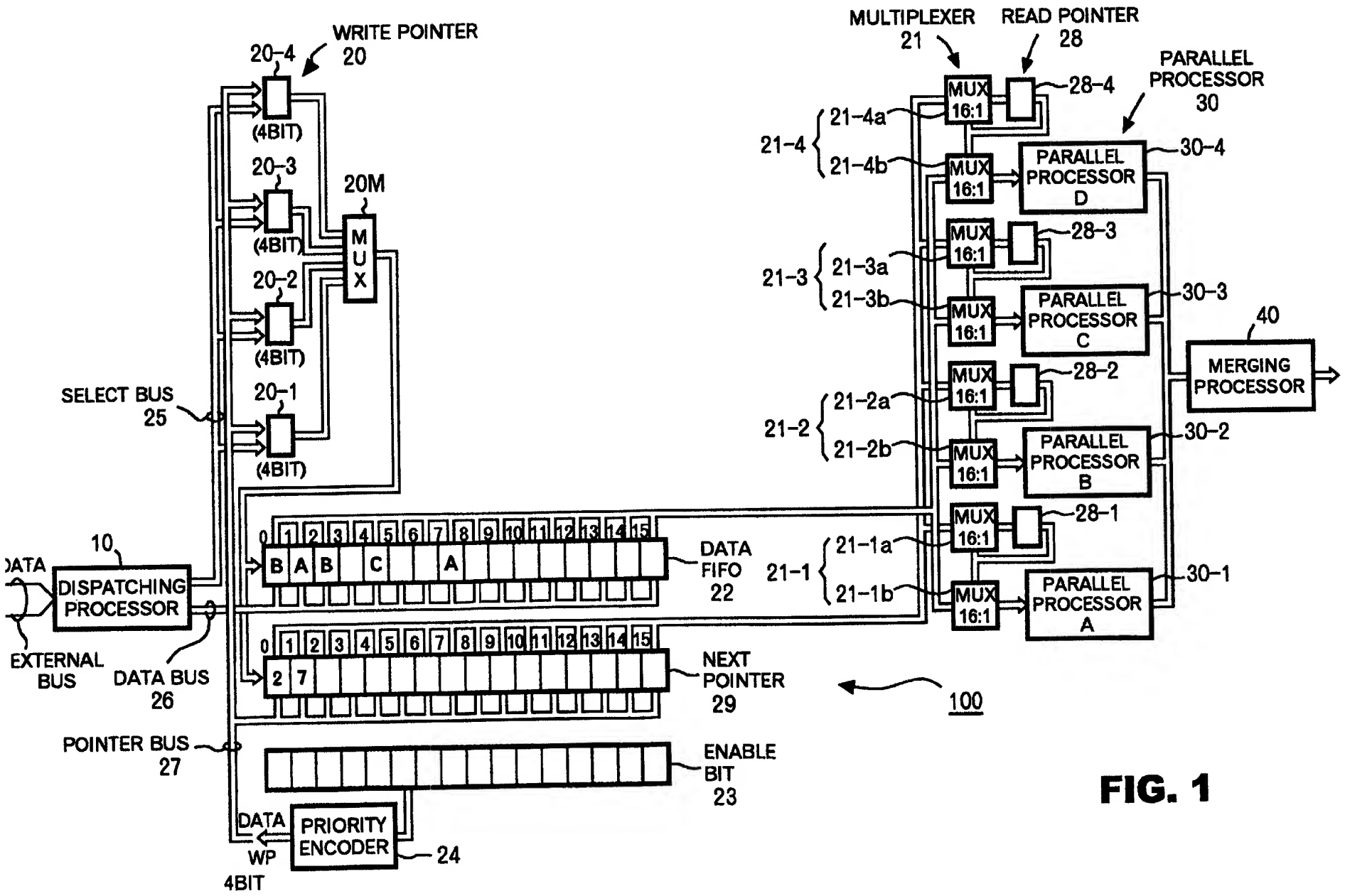

FIG. 1

TO6Tho 2705860

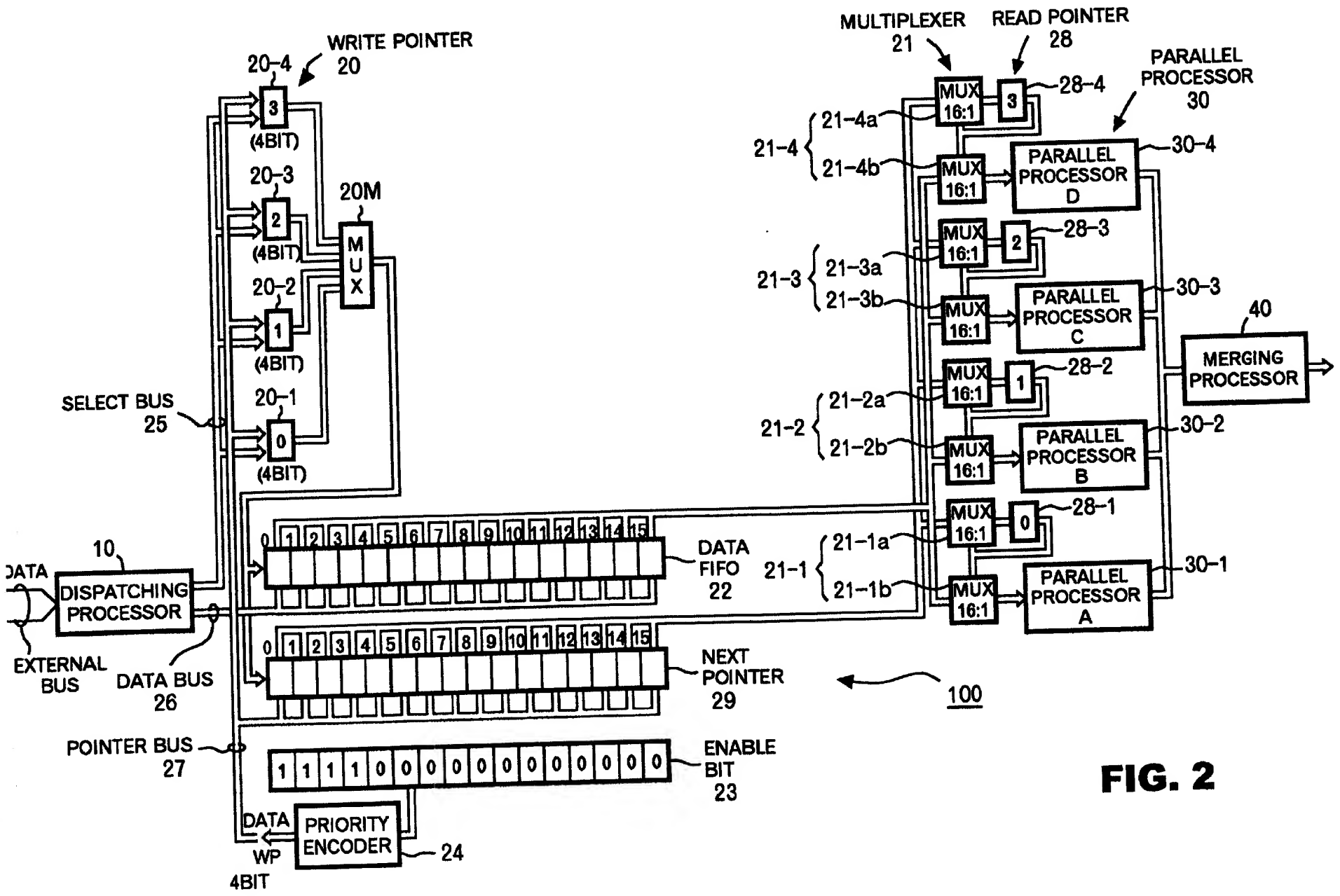

FIG. 2

TOSHIBA 2708960

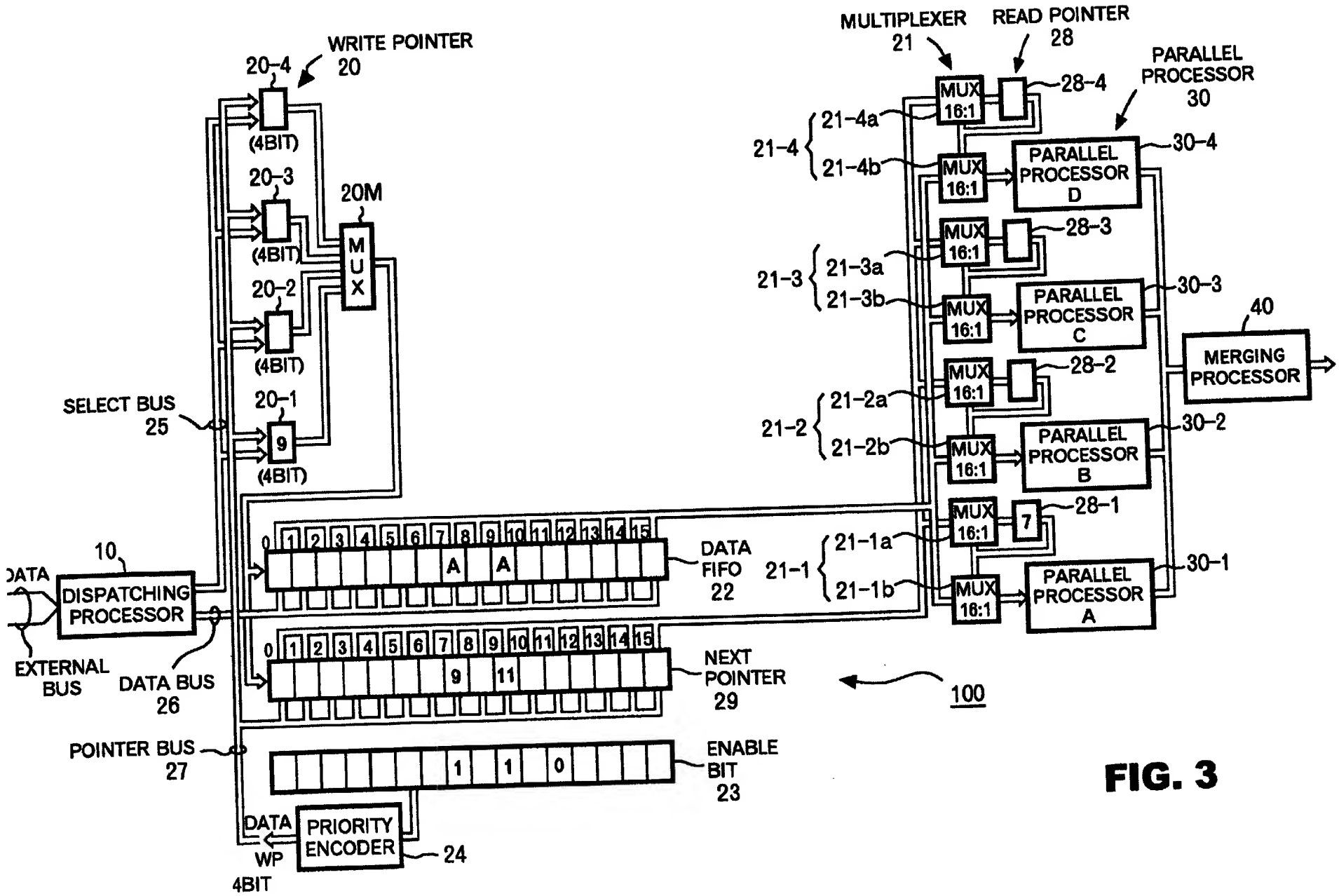

FIG. 3

TOSTAD" 2T08S60

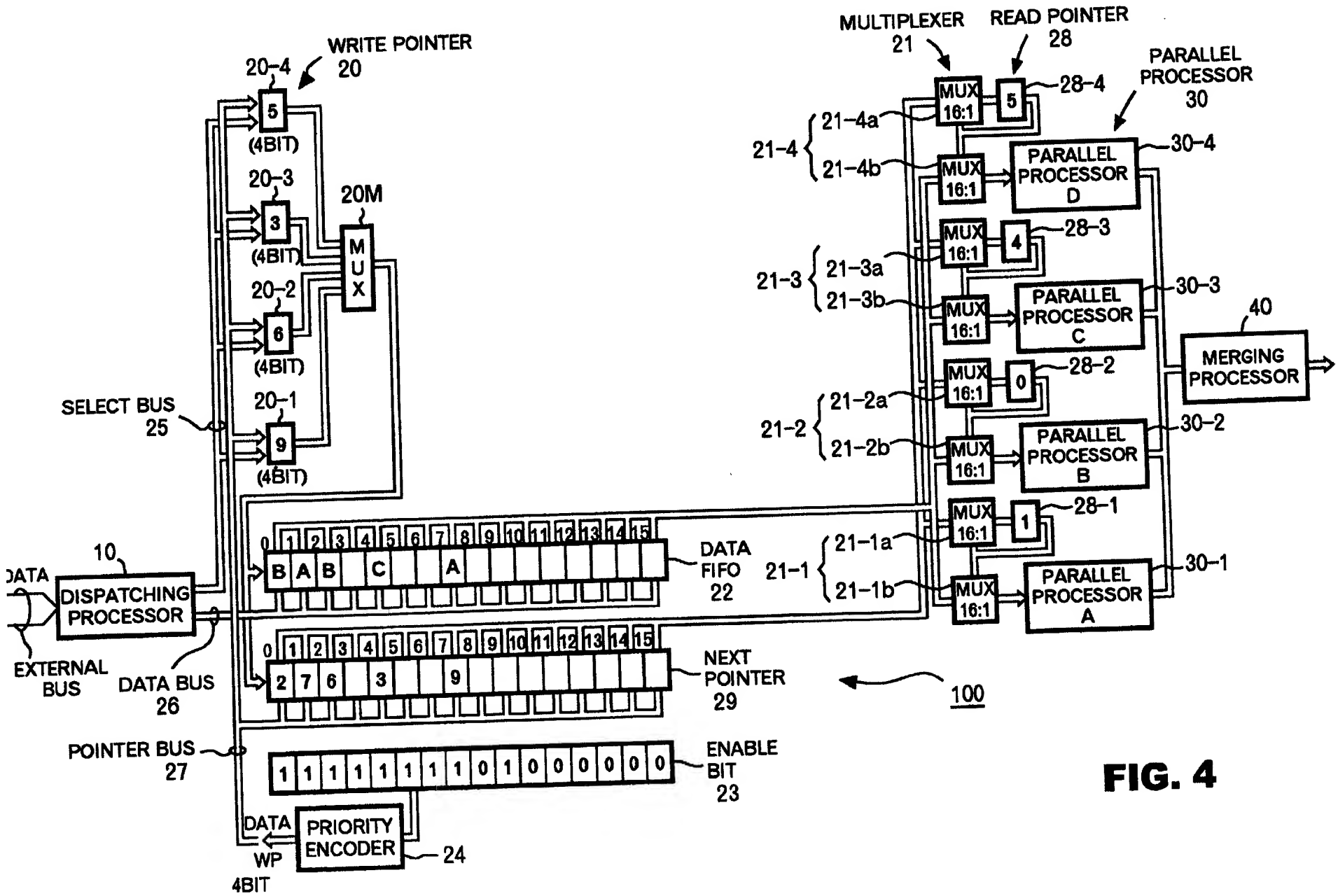

FIG. 4

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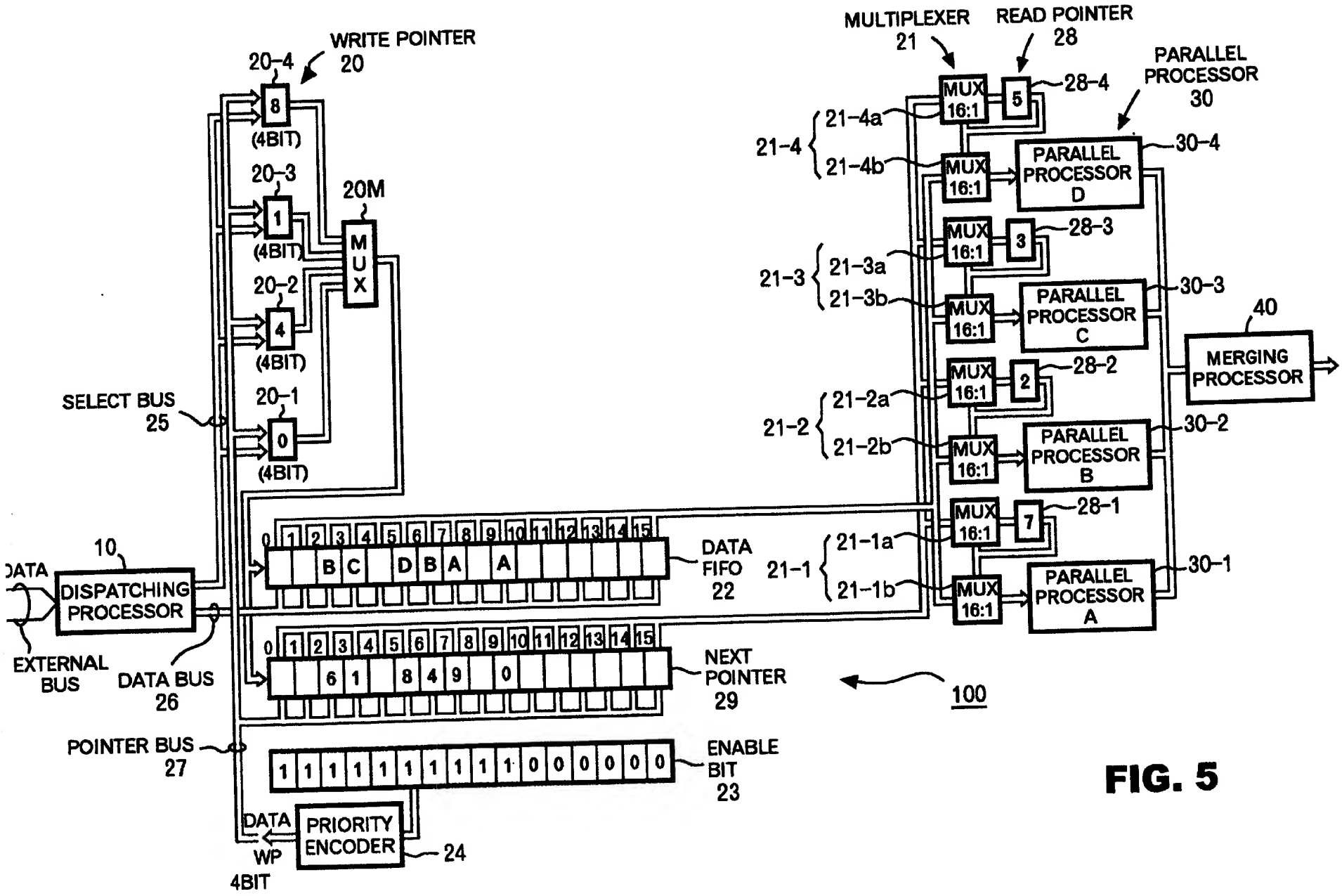

FIG. 5

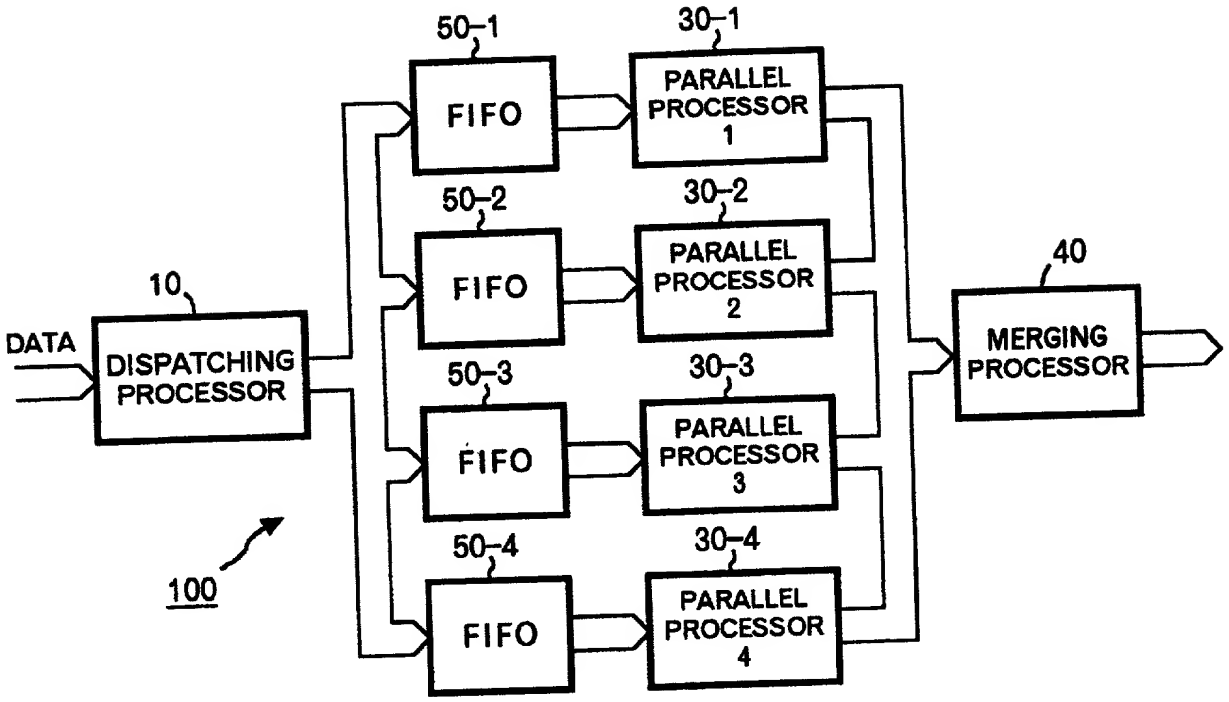

FIG. 6

	CASE(a)	CASE(b)	CASE(c)
PARALLEL PROCESSOR 1	1	2	2
PARALLEL PROCESSOR 2	3	1	8
PARALLEL PROCESSOR 3	2	5	1
PARALLEL PROCESSOR 4	1	1	3

FIG. 7

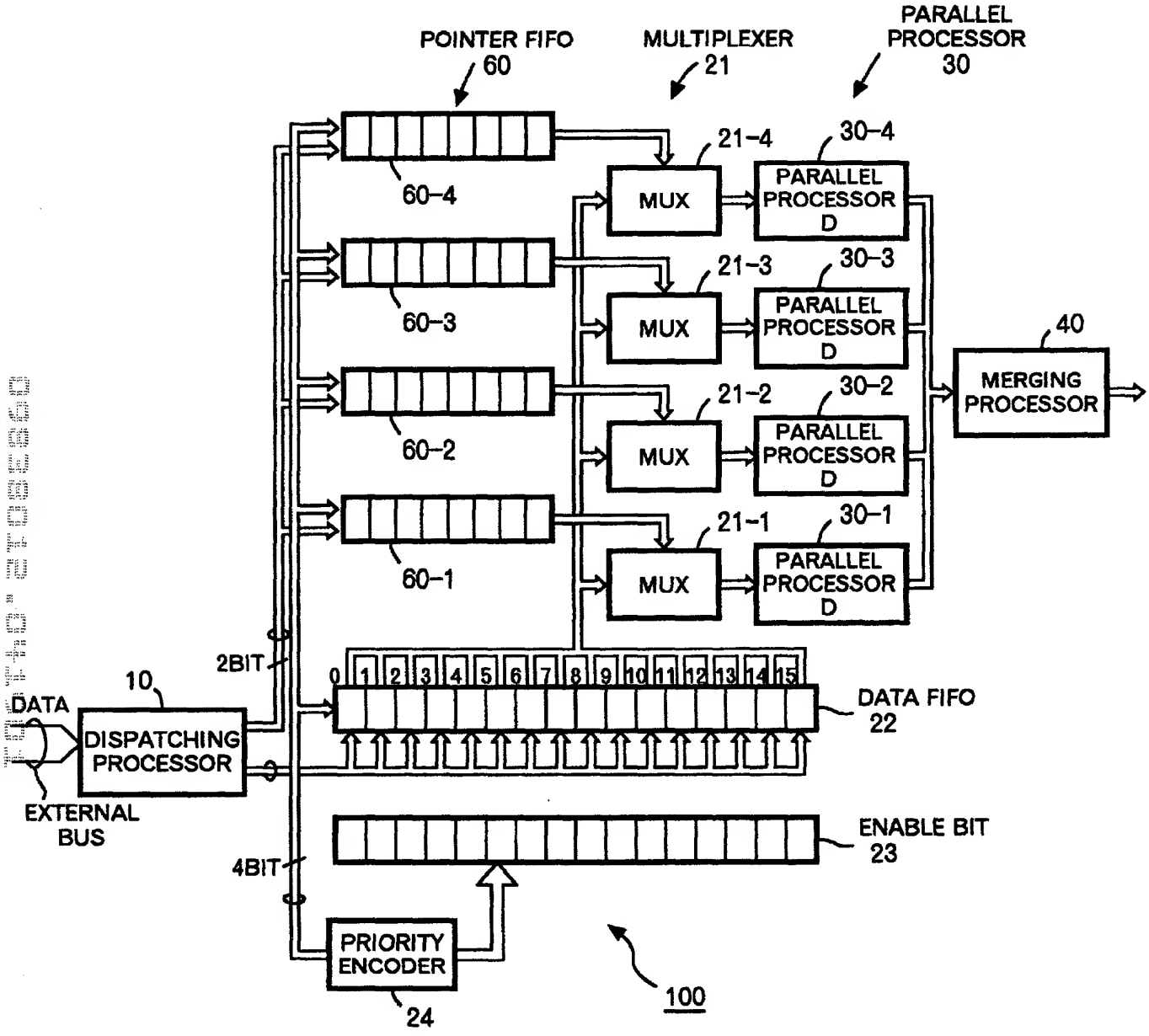
POINTER FIFO
60
MULTIPLEXER
21
PARALLEL
PROCESSOR
30
60-4
60-3
60-2
60-1
21-4
21-3
21-2
21-1
30-4
30-3
30-2
30-1
MUX
PARALLEL
PROCESSOR
D
40
MERGING
PROCESSOR
2BIT
10
DATA
DISPATCHING
PROCESSOR
EXTERNAL
BUS
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
DATA FIFO
22
ENABLE BIT
23
4BIT
PRIORITY
ENCODER
24
100

FIG. 8